

FIG. 1



18

Pin	Input/Output	Pin	Input/Output
TXD0	I	RXD0	O
TXD1	I	RXD1	O
TXD2	I	RXD2	O
TXD3	I	RXD3	O
TX_EN	I	RX_DV	O
TX_ER	O	RX_ER	O
TX_CLK	O	RX_CLK	O
MDIO	I/O	COL	O
MDC	I	CRS	O

FIG. 2

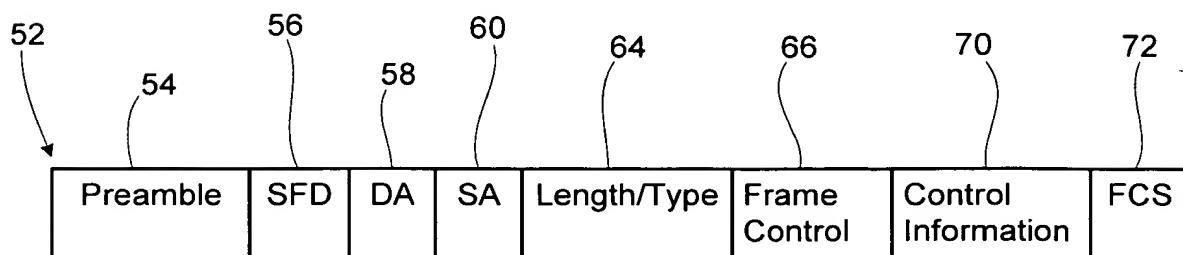


FIG. 3

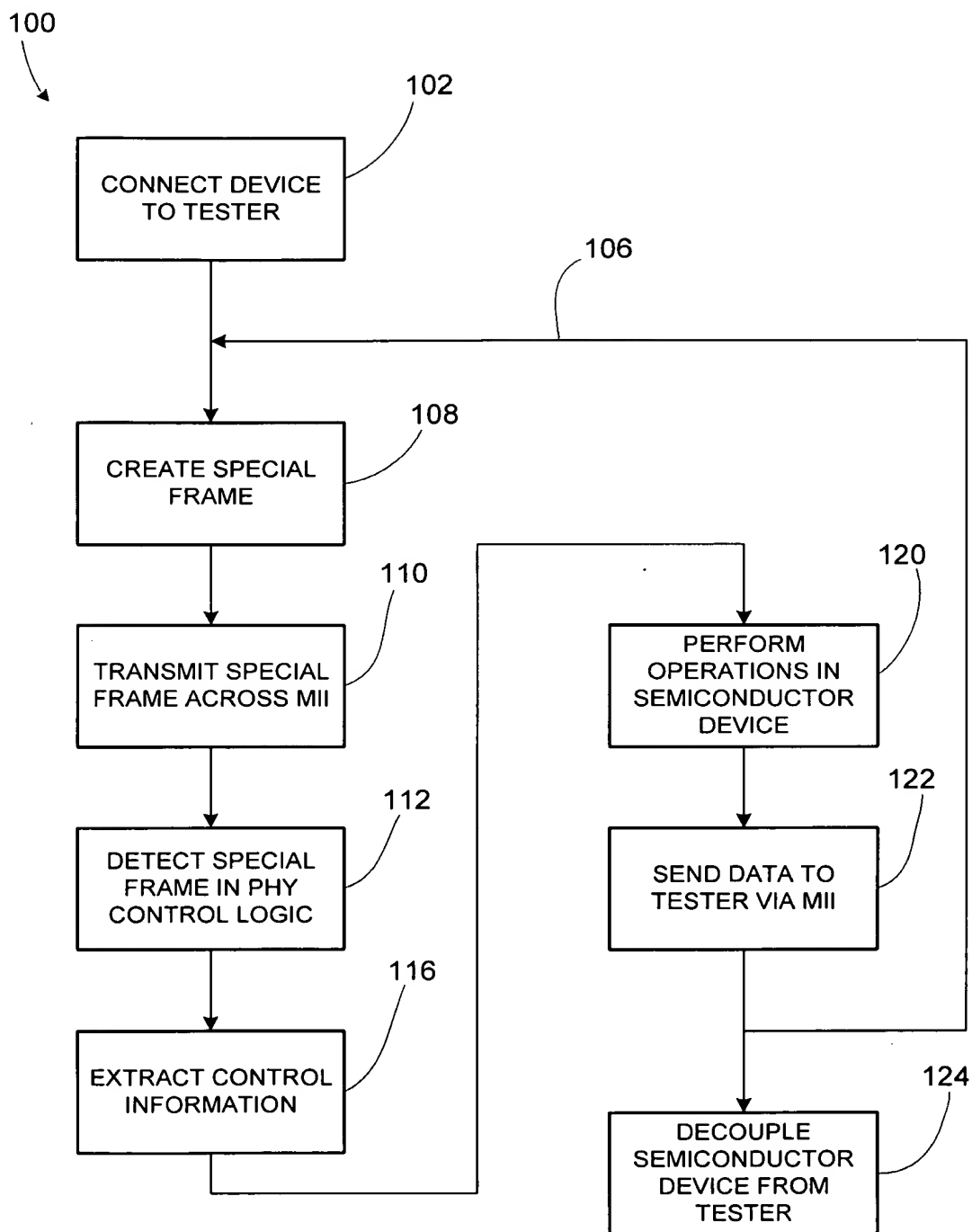


FIG. 4